

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takayasu ITO et al.

Appln. No.:

Filed: HERewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT

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PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-  
identified patent application as indicated below.

Amendments to the Specification begin on page 2 of  
this paper.

Remarks begin on page 3 of this paper.